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### (54) Complex electronic component

(57) The invention provides a complex electronic component, comprising; a multilayer substrate (11) formed by laminating a plurality of dielectric layers (11a - 11d); a first concave part (12) being provided at least at the bottom surface of said multilayer substrate (11); a filter comprising at least one first passive element disposed within said multilayer substrate (11); an external terminal (13) provided on at least one of the flat portion of the bottom surface and the side face of said multilayer substrate (11); either one of an active element (17) or a second passive element (18) mounted on the top sur-

face of said multilayer substrate (11), a surface acoustic wave element (14) mounted inside of said first concave portion (12) of said multilayer substrate (11); and a flat cap (16) provided at the opening of said first concave portion (12) of said multilayer substrate (11) so as to cover said opening of said first concave portion (12).

The size of the above complex electronic component is compact.

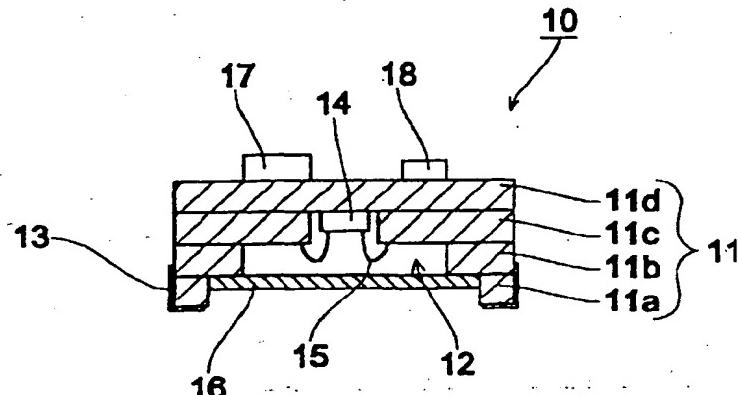


Fig. 1

**Description****BACKGROUND OF THE INVENTION****1. Field of the Invention**

[0001] The present invention relates to a complex electronic component to be used for, for example, mobile communication devices, especially to a complex electronic component in which a surface acoustic wave element is mounted in a concave portion of a multilayer substrate and a filter comprising a passive element is disposed within the multilayer substrate.

**2. Description of the Related Art**

[0002] FIG. 4 shows a common cross section of conventional complex electronic components 50 provided with a multilayer substrate 51 inside of which a LC filter is disposed. A concave portion 52 is provided on the top surface of the multilayer substrate 51 and a surface acoustic wave filter 53 as a bare chip is mounted inside of the concave portion 52, an IC 54 and a capacitor 55 being mounted on the top surface of the flat portion of the multilayer substrate 51 except the concave portion 52. The concave portion 52 is covered with a flat cap 56 made of a metal.

[0003] However, a concave portion for mounting the surface acoustic wave element and a flat portion for mounting the IC and capacitor should be provided on the top surface of the multilayer substrate in the conventional complex electronic component as described above. In other words, the size of the multilayer substrate becomes large due to two-dimensional arrangement of a plurality of components on the top surface of the multilayer substrate when the number of the component is increased, causing a problem that the size of the complex electronic component also becomes large.

**SUMMARY OF THE INVENTION**

[0004] The object of the present invention, carried out for solving the foregoing problems, is to provide a complex electronic component capable of being compacted.

[0005] The preferred embodiment of the present invention provides a complex electronic component, comprising: a multilayer substrate formed by laminating a plurality of dielectric layers; a first concave part being provided at least at the bottom surface of said multilayer substrate; a filter comprising at least one first passive element disposed within said multilayer substrate; an external terminal provided on at least one of the flat portion of the bottom surface and the side face of said multilayer substrate; either one of an active element or a second passive element mounted on the top surface of said multilayer substrate, a surface acoustic wave element mounted inside of said first concave portion of said multilayer substrate; and a flat cap provided at the

opening of said first concave portion of said multilayer substrate so as to cover said opening said first concave portion.

[0006] According to the above described complex electronic component, since either one of the active element or the passive element is mounted on the top surface of the multilayer substrate accommodating a filter comprising a passive element and the surface acoustic wave filter is mounted in the concave portion provided on the bottom surface of the multilayer substrate, the function required for the multilayer substrate can be allotted to the top and bottom surfaces to compact the multilayer substrate accommodating the filters. Consequently, the complex electronic component can be made compact thereby compacting the mobile communication device mounting the complex electronic component.

[0007] The above described complex electronic component may further comprise a second concave portion on said top surface of said multilayer substrate, and either said active element or said second passive element may be mounted on said second concave portion.

[0008] In the above structure, since the second concave portion is provided on the top surface of the multilayer substrate accommodating a filter comprising the passive element and either one of the active element or the passive element is mounted in the second concave portion, the height of the multilayer substrate can be made low, allowing the complex electronic component to be low height to result in a more compact size. Consequently, the mobile communication device mounting the complex electronic component becomes more compact.

[0009] Other features and advantages of the present invention will become apparent from the following description of the invention which refers to the accompanying drawings.

**BRIEF DESCRIPTION OF THE DRAWINGS****[0010]**

FIG. 1 is a cross section of the complex electronic component according to the first preferred embodiment of the present invention.

FIG. 2 is a cross section of the complex electronic component according to the second preferred embodiment of the present invention.

FIG. 3 is a block diagram of the compact size portable telephone as a mobile communication device.

FIG. 4 is a cross section showing the conventional complex electronic component.

**DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS**

[0011] FIG. 1 shows the cross section of the complex electronic component in the first preferred embodiment

according to the present invention. The complex electronic component 10 is provided with a multilayer substrate 11 inside of which a LC filter (not shown in the drawing) comprising a transfer circuit and capacitor are disposed, a first concave portion 12 being provided on the bottom surface of the multilayer substrate 11 and external electrodes 13 being provided on the faces from the bottom surface through the side face of the multilayer substrate 11. The multilayer substrate 11 as described above is produced, for example, by the following method.

[0012] Dielectric layers from 11a through 11d are prepared by laminating green sheets composed of a ceramic material sintered at a low temperature. Then, a required wiring pattern, strip electrodes for forming transfer circuits, a capacitor electrode for forming a capacitor and a ground electrode (not shown in the drawing) are printed on the dielectric layers from 11a through 11d using a copper paste. In the next step, a hole to serve as a first concave portion is provided in the dielectric layers from 11a through 11d by punching along with providing a hole for forming required via-hole electrodes (not shown in the drawing). The copper paste is filled into the hole for forming the via-hole electrode. The dielectric layers from 11a through 11d thus obtained are laminated and pressed, followed by baking at 960 °C for 1 hour in a neutral or weakly reducing atmosphere. Finally, the external electrodes 13 are formed by coating, followed by baking, the copper paste on the prescribed portion of the dielectric layer from the bottom surface through the side face.

[0013] A surface acoustic wave filter 14 (referred to as a SAW filter hereinafter), or a surface acoustic wave filter, is disposed as a bare tip inside of the first concave portion 12 of the multilayer substrate 11, obtained as described above. The filter is fixed by a tie-bonding and electrical continuity of the filter is accomplished through bonding wires 15 formed by a wire-bonding. The tie-bonding may also serve for electrical continuity.

[0014] After mounting the SAW filter 14 in the first concave portion 12, the first concave portion 12 is sealed with a flat cap 16. The flat cap 16 is composed of an arbitrary material such as a metal, a resin or a ceramic. When the flat cap 16 is composed of a metal, a metal film is previously formed on the portion of the multilayer substrate 11 making a contact with the flat cap 16 to join with the flat cap 16 by burying a solder into the gap between the flat cap 16 and the multilayer substrate 11. When the flat cap 16 is composed of a resin or a ceramic, the flat cap 16 is adhered to the multilayer substrate 11 with a heat curing type adhesive. Otherwise, The first concave portion 12 may be filled with a resin.

[0015] The complex electronic component 10 is completed by mounting an IC 17 as an active element and a capacitor 18 as a passive element on the flat top surface of the multilayer substrate 11 after sealing the first concave portion 12 provided on the bottom surface of the multilayer substrate 11 with the flat cap 16.

[0016] Characteristics of the complex electronic component are evaluated thereafter for respective complex electronic components 10 via the external electrodes 13, provided on the side faces of the multilayer substrate 11 to discriminate between the good and defective products.

[0017] The construction of the complex electronic component according to the first preferred embodiment of the present invention, mounting the IC 17 and capacitor on the top surface of the multilayer substrate accommodating the LC filter and the SAW filter in the first concave portion provided on the bottom surface of the multilayer substrate, allows the function required for the multilayer substrate to be allotted to the top and bottom surfaces, thereby making the multilayer substrate accommodating the filter small to result in a compact size complex electronic component.

[0018] FIG. 2 shows the cross section of the complex electronic component according to the second preferred embodiment of the present invention. The complex electronic component 20 is provided with a multilayer substrate 21 inside of which a LC filter (not shown in the drawing) comprising a transfer circuit and a capacitor is disposed, the first concave portion 22 and the second concave portion 23 being provided on the top and bottom surfaces of the multilayer substrate 21, respectively, and external electrodes being provided on the side faces of the multilayer substrate 21. The multilayer substrate 21 is produced by the same method as described in producing the multilayer substrate 11 in the first embodiment.

[0019] The surface acoustic wave filter 25 (referred to as a SAW filter hereinafter), or a surface acoustic wave element, is disposed within the first concave portion 22 provided at the bottom surface of the multilayer substrate 21 as a bare tip, which is fixed with a tie-bonding forming an electric continuity with the bonding wire 26 formed of a wire bonding. The tie-bonding may also serve for electric continuity.

[0020] The first concave portion 22 is sealed with the flat cap 27 after mounting the SAW filter 25 in the first concave portion 22. The flat cap 27 is constructed of an arbitrary material such as a metal, a resin or a ceramic. Likewise the case in the first embodiment, a metal film is previously formed on the portion of the multilayer substrate 21 to make a contact with the flat cap 27 to join the multilayer substrate 21 with the flat cap 27 by burying a solder into the gap between the flat cap 27 and multilayer substrate 21 when the flat cap 27 is composed of a metal. When the flat cap 27 is composed of a resin or a ceramic, the flat cap 16 is adhered to the multilayer substrate 21 with a heat curing type adhesive. Otherwise, The first concave portion 21 may be filled with a resin.

[0021] After sealing the first concave portion 22 provided on the bottom surface of the multilayer substrate 21 with the flat cap 27, the LC 28 as an active element and the capacitor 29 as a passive element are mounted.

in the second concave portion 23 provided on the top surface, thereby completing the complex electronic component 20.

[0022] Characteristics of the complex electronic component are evaluated thereafter for respective complex electronic components 20 via the external electrodes 24 provided on the side faces of the multilayer substrate 21 to discriminate between the good and defective products.

[0023] According to the complex electronic component in the second preferred embodiment hitherto described, the second concave portion is provided on the top surface of the multilayer substrate accommodating the LC filter to allow the IC and capacitor to be mounted in the second concave portion, thereby enabling to make the height of the multilayer substrate low for more compacting the component.

[0024] FIG. 3 shows a block-diagram of a compact size portable telephone (Portable Handyphone System; PHS) as an example of mobile communication devices. In FIG. 3, ANT is an antenna, SW is a switch for allotting and coupling high frequency signals in different frequency bands, LPF1 is a low-pass filter as a first filter of a transmission (Tx) side, PA is a high output amplifier at the Tx side, BPF is a band-pass filter as a second filter at the Tx side, LNA is a low-noise amplifier at the transmission (Rx) side and LPF2 is a low-pass filter as a filter at the Rx side.

[0025] On the premise that the LC filter (FIG. 1 and FIG. 2) accommodated in the multilayer substrate 11 or 21 of the complex electronic component 10 or 20 corresponds to the low-pass filter LPF1, the SAW filter 14 or 25 (FIG. 1 and FIG. 2) mounted in the concave portion 12 or 22 on the bottom surface of the multilayer substrate 11 or 21 corresponds to the band-pass filter BPF and the IC 17 or 28 (FIG. 1 and FIG. 2) mounted on the top surface or in the second concave portion 23 of the multilayer substrates 11 or 21 corresponds to the high output amplifier PA in the PHS having the construction as described above, the part indicated by the broken line in FIG. 3 can be constructed by one complex electronic component 10 or 20 (FIG. 1 or FIG. 2) to enable compacting the PHS.

[0026] While the cases in which both of the active and passive elements are mounted on the top surface of the multilayer substrate or in the concave portion formed on the top surface of the multilayer were explained in the foregoing embodiments, the same effect can be obtained when only the active element or the passive element is mounted.

[0027] Although the case when the active and passive elements to be mounted on the top surface or in the concave portion on the top surface of the substrate are tip components was described, the same effect can be attained when the tips are mounted as bare-tips. Reliability may be well improved by covering the active and passive elements as bare-tips with a resin.

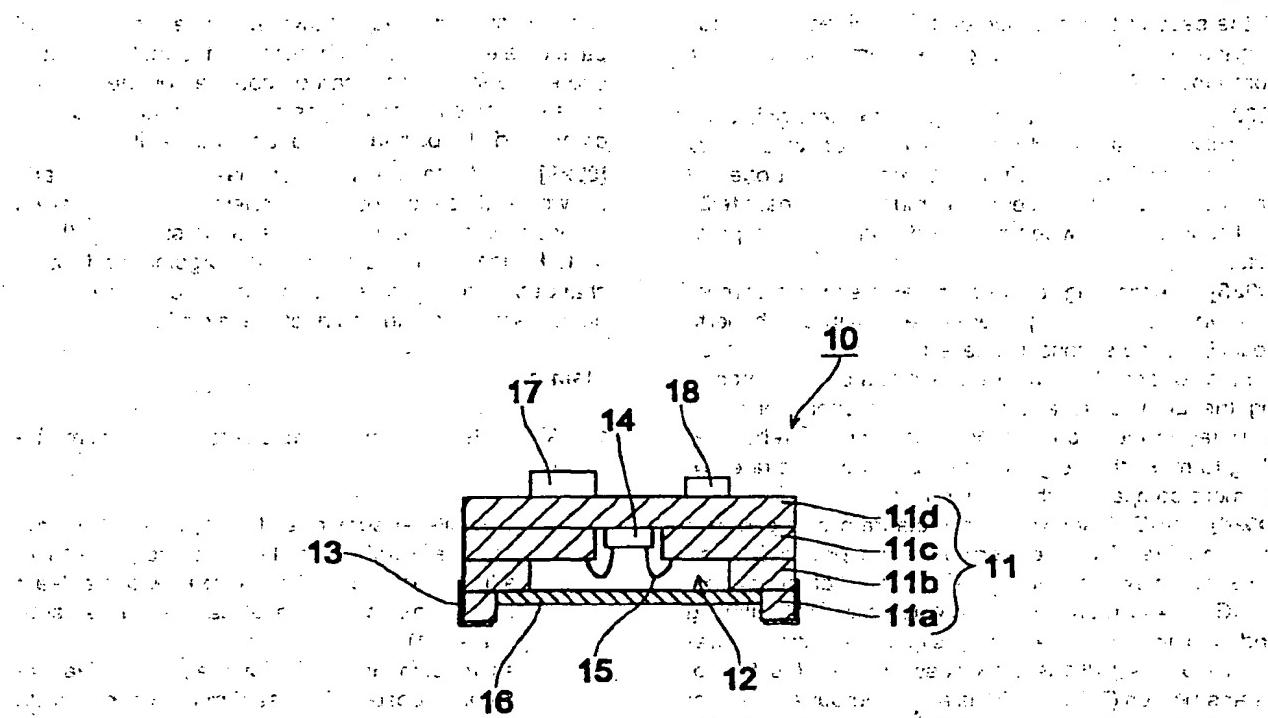
[0028] While the LC filter composed of a transfer cir-

cuit and capacitor was shown as a filter composed of passive elements accommodated in the multilayer substrate, the same effect can be obtained by the LC filter merely composed of a transfer circuit and a RC filter composed of a built-in resistor and a capacitor.

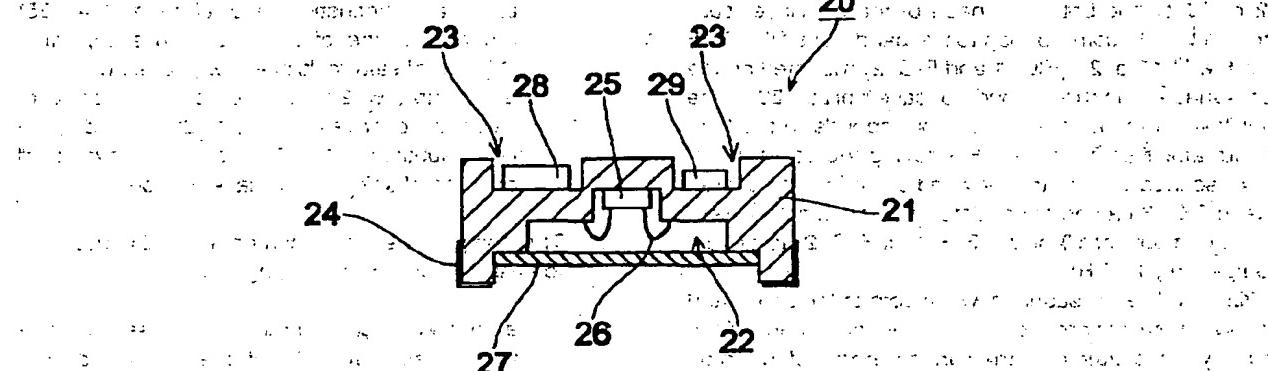
[0029] While the invention has been particularly shown and described with reference to preferred embodiments thereof, it will be understood by those skilled man in the art that the forgoing and other changes in form and details may be made therein without departing from the spirit of the invention.

### Claims.

15. 1. A complex electronic component (10; 20), comprising:  
a multilayer substrate (11; 21) formed by laminating a plurality of dielectric layers (11a - 11d);  
a first concave part (12) being provided at least at the bottom surface of said multilayer substrate (11);  
a filter comprising at least one first passive element disposed within said multilayer substrate (11; 21);  
an external terminal (13; 24) provided on at least one of the flat portion of the bottom surface and the side face of said multilayer substrate (11; 21);  
either one of an active element (17; 28) or a second passive element (18; 29) mounted on the top surface of said multilayer substrate (11; 21);  
a surface acoustic wave element (14; 25) mounted inside of said first concave portion (12; 22) of said multilayer substrate; and  
a flat cap (16; 27) provided at the opening of said first concave portion (12; 22) of said multilayer substrate (11; 21) so as to cover said opening of said first concave portion.
20. 2. The complex electronic component (20) according to Claim 1, further comprising:  
a second concave portion (23) on said top surface of said multilayer substrate (21); and  
either said active element (28) or said second passive element (29) being mounted on said second concave portion (23).
25. 3. The complex electronic component (20) according to Claim 1, further comprising:  
a second concave portion (23) on said top surface of said multilayer substrate (21); and  
either said active element (28) or said second passive element (29) being mounted on said second concave portion (23).
30. 4. The complex electronic component (20) according to Claim 1, further comprising:  
a second concave portion (23) on said top surface of said multilayer substrate (21); and  
either said active element (28) or said second passive element (29) being mounted on said second concave portion (23).
35. 5. The complex electronic component (20) according to Claim 1, further comprising:  
a second concave portion (23) on said top surface of said multilayer substrate (21); and  
either said active element (28) or said second passive element (29) being mounted on said second concave portion (23).
40. 6. The complex electronic component (20) according to Claim 1, further comprising:  
a second concave portion (23) on said top surface of said multilayer substrate (21); and  
either said active element (28) or said second passive element (29) being mounted on said second concave portion (23).
45. 7. The complex electronic component (20) according to Claim 1, further comprising:  
a second concave portion (23) on said top surface of said multilayer substrate (21); and  
either said active element (28) or said second passive element (29) being mounted on said second concave portion (23).
50. 8. The complex electronic component (20) according to Claim 1, further comprising:  
a second concave portion (23) on said top surface of said multilayer substrate (21); and  
either said active element (28) or said second passive element (29) being mounted on said second concave portion (23).
55. 9. The complex electronic component (20) according to Claim 1, further comprising:  
a second concave portion (23) on said top surface of said multilayer substrate (21); and  
either said active element (28) or said second passive element (29) being mounted on said second concave portion (23).



**Fig. 1**



**Fig. 2**

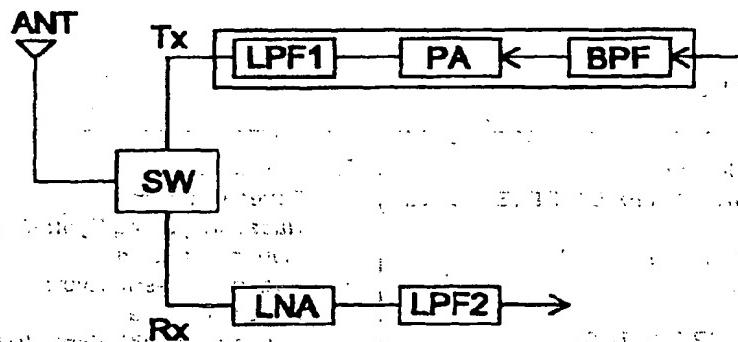


Fig. 3

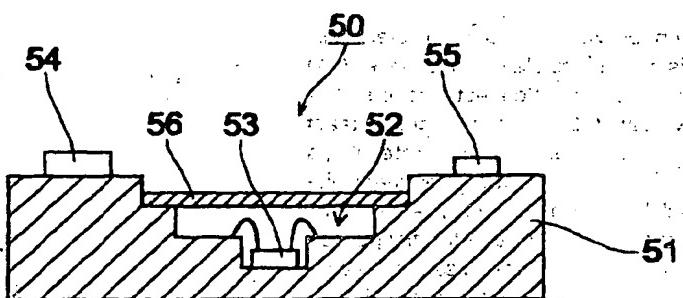


Fig. 4



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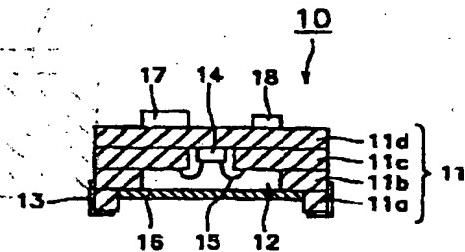
Schoppe, Fritz, Dipl.-Ing.  
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## (54) Complex electronic component

(57) The invention provides a complex electronic component, comprising: a multilayer substrate (11) formed by laminating a plurality of dielectric layers (11a - 11d); a first concave part (12) being provided at least at the bottom surface of said multilayer substrate (11); a filter comprising at least one first passive element disposed within said multilayer substrate (11); an external terminal (13) provided on at least one of the flat portion of the bottom surface and the side face of said multilayer substrate (11); either one of an active element (17) or a second passive element (18) mounted on the top surface of said multilayer substrate (11), a surface acoustic wave element (14) mounted inside of said first concave portion (12) of said multilayer substrate (11); and a flat cap (16) provided at the opening of said first concave portion (12) of said multilayer substrate (11) so as to cover said opening of said first concave portion (12).

The size of the above complex electronic component is compact.

FIG. 1



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## EUROPEAN SEARCH REPORT

Application Number  
EP 98 12 0026

DOCUMENTS CONSIDERED TO BE RELEVANT			CLASSIFICATION OF THE APPLICATION (Int.Cl.6)
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	
X	DE 196 21 353 A (FUJITSU LTD) 20 February 1997 * column 1, line 46 - line 61 * * column 5, line 50 - column 6, line 27 * * column 9, line 44 - line 56 * * figures 1C,2A,2B,9 *	1,2	H03H9/05
X	US 5 422 615 A (SHIBAGAKI NOBUHIKO ET AL) 6 June 1995 * column 4, line 11 - line 65; figures 1,10 *	1,2	
			TECHNICAL FIELDS SEARCHED (Int.Cl.6)
			H03H
<p>The present search report has been drawn up for all claims</p>			
Place of search	Date of completion of the search	Examiner	
THE HAGUE	1 April 1999	D/L PINTA BALLE.., L	
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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
DE 19621353 A	20-02-1997	JP 9051206 A US 5786738 A	18-02-1997 28-07-1998
US 5422615 A	06-06-1995	JP 6236815 A	23-08-1994

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